DESCRIPTION

This series of fixed-voltage monolithic integrated circuit voltage regulators is designed for a wide range of applications. These applications include on-card regulation for elimination of noise and distribution problems associated with single-point regulation. In addition, they can be used with power-pass elements to make high-current voltage regulators. Each of these regulators can deliver up to 100mA of output current. The internal limiting and termal shutdown features of these regulators make them essentially immune to overload.

When used as a replacement for a Zener diode-resistor combination , an effective improvement in output impedance can be obtained together with lower-bias current.

FEATURES

- 3-Terminal Regulators
- Output Current Up to 100mA
- Fixed Output Voltages of -5V -6V -8V -12V -15V -18V - 24V
- No External Components
- Internal Thermal Overload Protection
- Internal Short-Circuit Limiting
- Direct Replacement for Motorola MC79L00 Series

Absolute maximum ratings over operating temperature range (unless otherwise noted)

	79L05 thru 79L08	79L12 thru 79L18	79L24	UNIT
Input voltage	-30	-35	-40	V
Operating free-air, case, or virtual junction temperature range	0 to 150	0 to 150	0 to 150	°C
Storage temperature range	-65 to 150	-65 to 150	-65 to 150	
Lead temperature 1.6 mm (1/16 inch) from case for 10 seconds	260	260	260	

Recommended operating conditions

Parameter		MIN	MAX	UNIT
Input voltage, V_I	79L05	-7	-20	V
	79L06	-8	-20	
	79L08	-10.5	-23	
	79L12	-14.5	-27	
	79L15	-17.5	-30	
	79L18	-20.5	-33	
	79L24	-27	-38	
Output current, Io			100	mA
Operating temperature range, T_J		-40	125	°C

79L05 electrical characteristics at specified virtual junction temperature, VI=-10V, IO=40mA (unless otherwise noted)

PARAMETER	TEST CONDITIONS*		79L05			UNIT
			MIN	TYP	MAX	
Output voltage**		25°C	-4.8	-5	-5.2	V
	I_O=1mA to 40mA V_I=-7V to -20V	0 to 125 °C	-4.75	-5	-5.25	
	I_O=1mA to 70mA		-4.75	-5	-5.25	
Input regulation	V_I=-7V to -20V	25°C		15	150	mV
	V_I=-8V to -20V			12	100	
Ripple rejection	V_I=-8V to -18V, f=120Hz	25°C	41	49		dB
Output regulation	I_O=1mA to 100mA	25°C		20	60	mV
	I_O=1mA to 40mA			10	30	
Output noise voltage	f=10Hz-100Hz	25°C		40		μV
Dropout voltage		25°C		1.7		V
Bias current		25°C		3.8	6	mA
		125°C			5.5	
Bias current change	V_I=-8V to -20V	0 to 125 °C			1.5	
	I_O=1mA to 40mA				0.1	

*Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33μF capacitor across the input and a 0.1μF capacitor across the output.

**This specification applies only for dc power dissipation permitted by absolute maximum ratings.

79L06 electrical characteristics at specified virtual junction temperature, VI=-11V, IO=40mA (unless otherwise noted)

PARAMETER	TEST CONDITIONS*		79L06			UNIT
			MIN	TYP	MAX	
Output voltage**		25°C	-5.75	-6	-6.25	V
	I_O=1mA to 40mA V_I=-8V to -20V	0 to 125 °C	-5.7	-6	-6.3	
	I_O=1mA to 70mA		-5.7	-6	-6.3	
Input regulation	V_I=-8V to -20V	25°C		20	175	mV
	V_I=-9V to -20V			15	125	
Ripple rejection	V_I=-9V to -19V, f=120Hz	25°C	40	48		dB
Output regulation	I_O=1mA to 100mA	25°C		21	80	mV
	I_O=1mA to 40mA			11	40	
Output noise voltage	f=10Hz-100Hz	25°C		44		μV
Dropout voltage		25°C		1.7		V
Bias current		25°C			6	mA
		125°C			5.5	
Bias current change	V_I=-9V to -20V	0 to 125 °C			1.5	
	I_O=1mA to 40mA				0.1	

79L08 electrical characteristics at specified virtual junction temperature, VI=-14V, IO=40mA (unless otherwise noted)

PARAMETER	TEST CONDITIONS*		79L08			UNIT
			MIN	TYP	MAX	
Output voltage**		25°C	-7.7	-8	-8.3	V
	I_O=1mA to 40mA V_I=-10.5V to -23V	0 to 125 °C	-7.6	-8	-8.4	
	I_O=1mA to 70mA		-7.6	-8	-8.4	
Input regulation	V_I=-10.5V to -23V	25°C		42	200	mV
	V_I=-11V to -23V			36	150	
Ripple rejection	V_I=-13V to -23V, f=120Hz	25°C	37	46		dB
Output regulation	I_O=1mA to 100mA	25°C		30	100	mV
	I_O=1mA to 40mA			15	50	
Output noise voltage	f=10Hz-100Hz	25°C		54		μV
Dropout voltage		25°C		1.7		V
Bias current		25°C			6	mA
		125°C			5.5	
Bias current change	V_I=-11V to -23V	0 to 125 °C			1.5	
	I_O=1mA to 40mA				0.1	

*Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33μF capacitor across the input and a 0.1μF capacitor across the output.

**This specification applies only for dc power dissipation permitted by absolute maximum ratings.

79L12 electrical characteristics at specified virtual junction temperature, VI=-19V, IO=40mA (unless otherwise noted)

PARAMETER	TEST CONDITIONS*		79L12			UNIT
			MIN	TYP	MAX	
Output voltage**		25°C	-11.5	-12	-12.5	V
	I_O=1mA to 40mA V_I=-14V to -27V	0 to 125 °C	-11.4	-12	-12.6	
	I_O=1mA to 70mA		-11.4	-12	-12.6	
Input regulation	V_I=-14V to -27V	25°C		50	250	mV
	V_I=-16V to -27V			40	200	
Ripple rejection	V_I=-15V to -25V, f=120Hz	25°C	37	42		dB
Output regulation	I_O=1mA to 100mA	25°C		24	100	mV
	I_O=1mA to 40mA			15	50	
Output noise voltage	f=10Hz-100Hz	25°C		80		μV
Dropout voltage		25°C		1.7		V
Bias current		25°C			6.5	mA
		125°C			6	
Bias current change	V_I=-16V to -27V	0 to 125 °C			1.5	
	I_O=1mA to 40mA				0.1	

79L15 electrical characteristics at specified virtual junction temperature, VI=-23V, IO=40mA (unless otherwise noted)

PARAMETER	TEST CONDITIONS*		79L15			UNIT
			MIN	TYP	MAX	
Output voltage**		25°C	-14.4	-15	-15.6	V
	I_O=1mA to 40mA V_I=-17.5V to -30V	0 to 125 °C	-14.25	-15	-15.75	
	I_O=1mA to 70mA		-14.25	-15	-15.75	
Input regulation	V_I=-17.5V to -30V	25°C		65	300	mV
	V_I=-19V to -30V			50	250	
Ripple rejection	V_I=-18.5V to -28.5V, f=120Hz	25°C	34	39		dB
Output regulation	I_O=1mA to 100mA	25°C		25	150	mV
	I_O=1mA to 40mA			15	75	
Output noise voltage	f=10Hz-100Hz	25°C		90		μV
Dropout voltage		25°C		1.7		V
Bias current		25°C			6.5	mA
		125°C			6	
Bias current change	V_I=-19V to -30V	0 to 125 °C			1.5	
	I_O=1mA to 40mA				0.1	

*Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33μF capacitor across the input and a 0.1μF capacitor across the output.

**This specification applies only for dc power dissipation permitted by absolute maximum ratings.

79L18 electrical characteristics at specified virtual junction temperature, VI=-26V, IO=40mA (unless otherwise noted)

PARAMETER	TEST CONDITIONS*		79L18			UNIT
			MIN	TYP	MAX	
Output voltage**		25°C	-17.3	-18	-18.7	V
	I_O=1mA to 40mA V_I=-20.5V to -33V	0 to 125 °C	-17.1	-18	-18.9	
	I_O=1mA to 70mA		-17.1	-18	-18.9	
Input regulation	V_I=-20.5V to -33V	25°C		70	325	mV
	V_I=-22V to -33V			60	275	
Ripple rejection	V_I=-21.5V to -31.5V, f=120Hz	25°C	33	48		dB
Output regulation	I_O=1mA to 100mA	25°C		27	170	mV
	I_O=1mA to 40mA			19	85	
Output noise voltage	f=10Hz-100Hz	25°C		150		μV
Dropout voltage		25°C		1.7		V
Bias current		25°C			6.5	mA
		125°C			6	
Bias current change	V_I=-22V to -33V	0 to 125 °C			1.5	
	I_O=1mA to 40mA				0.1	

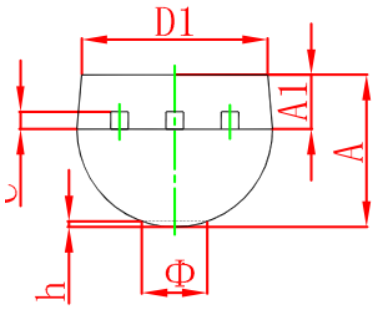
79L24 electrical characteristics at specified virtual junction temperature, VI=-33V, IO=40mA (unless otherwise noted)

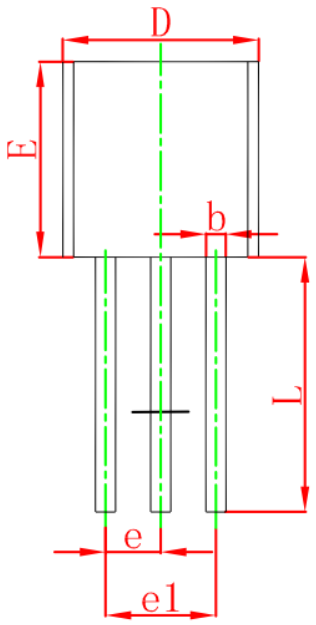
PARAMETER	TEST CONDITIONS*		79L24			UNIT
			MIN	TYP	MAX	
Output voltage**		25°C	-23	-24	-25	V
	I_O=1mA to 40mA V_I=-27V to -38V	0 to 125 °C	-22.8	-24	-25.2	
	I_O=1mA to 70mA		-22.8	-24	-25.2	
Input regulation	V_I=-27 V to -38V	25°C		90	350	mV
	V_I=-28V to -38V			75	300	
Ripple rejection	V_I=-29V to -35V, f=120Hz	25°C	31	47		dB
Output regulation	I_O=1mA to 100mA	25°C		40	200	mV
	I_O=1mA to 40mA			25	100	
Output noise voltage	f=10Hz-100Hz	25°C		200		μV
Dropout voltage		25°C		1.7		V
Bias current		25°C			6.5	mA
		125°C			6	
Bias current change	V_I=-28V to -38V	0 to 125 °C			1.5	
	I_O=1mA to 40mA				0.1	

*Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33μF capacitor across the input and a 0.1μF capacitor across the output.

**This specification applies only for dc power dissipation permitted by absolute maximum ratings.

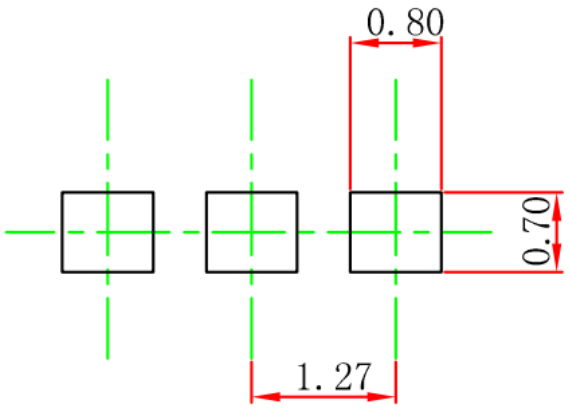
TO-92 Package Outline Dimensions





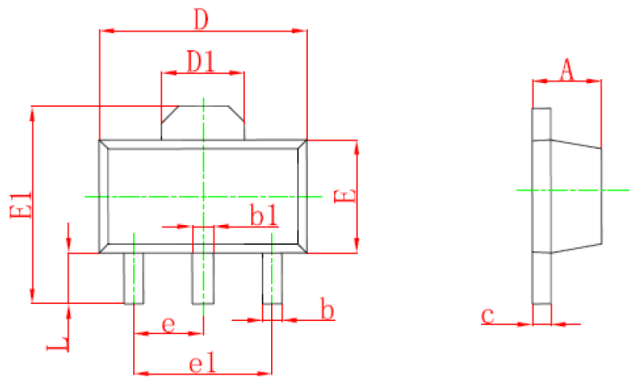
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	3.300	3.700	0.130	0.146
A1	1.100	1.400	0.043	0.055
b	0.380	0.550	0.015	0.022
c	0.360	0.510	0.014	0.020
D	4.300	4.700	0.169	0.185
D1	3.430		0.135	
E	4.300	4.700	0.169	0.185
e	1.270 TYP		0.050 TYP	
e1	2.440	2.640	0.096	0.104
L	14.100	14.500	0.555	0.571
Φ		1.600		0.063
h	0.000	0.380	0.000	0.015

TO-92 Suggested Pad Layout



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:± 0.05mm.
3.The pad layout is for reference purposes only.

SOT-89-3L Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF.		0.061 REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP.		0.060 TYP.	
e1	3.000 TYP.		0.118 TYP.	
L	0.900	1.200	0.035	0.047